

ABSTRACT OF THE DISCLOSURE

An RF receiver includes an analog GO circuit that recognizes the presence of a data packet within a received RF signal in the receiver's analog gain control section, and in response produces an analog GO signal, and a digital GO circuit that recognizes the presence of a data packet from the receiver's digital demodulator section, and in response produces a digital GO signal. Gain control functions within the analog gain control section, and digital demodulator functions within the digital demodulator section, are enabled in response to respective analog and digital GO signals. The system is capable of distinguishing a valid data packet from noise, co-channel interference and adjacent channel interference. A symbol timing recovery circuit within the receiver identifies periodic samples of a data packet preamble that most closely match the preamble bits, with the receiver employing a continuation of the identified periodic samples to decode the remainder of the data packet.